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## (54) AIS TRANSMISSION CIRCUIT

## (57) Abstract:

PURPOSE: To use other clock to send an AIS signal when a PLL circuit of a channel has a fault by connecting a read clock of each channel at a receiver side in parallel with a selector of each channel at a low-order group of an asynchronous digital multiplexer.

CONSTITUTION: Receiver side read clocks 121-124 for each channel selected by selectors 21-24 are respectively inputted to receiver buffer memories 31-34. The selectors 2<sub>1</sub>-2<sub>4</sub> are subject to selection control by receiver side read signal interrupt signals 131134. Thus, an AIS(Alarm Indication Signal) signal is sent by using a read clock of other PLL circuit when any of PLL circuits 11-14 has a fault in a channel through the provision of the selectors 21-24 connecting in parallel with each channel of outputs of the PLL circuits 11-14 of each channel of a receiver side of a low order.

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